

High-reliability discrete products and engineering services since 1977

## SC149

## SILICON BIDIRECTIONAL THYRISTORS

#### **FEATURES**

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Repetitive peak off-stage voltage, gate open			
SC149B		200	
SC149D	$V_{DRM}$	400	Volts
SC149E		500	
SC149M		600	
RMS on-state current (T <sub>C</sub> = 80°C)	I <sub>T(RMS)</sub>	12	Amps
Peak non-repetitive surge current (One Cycle, 60Hz)	I <sub>TSM</sub>	120	Amps
Circuit fusing considerations	l <sup>2</sup> t		A <sup>2</sup> s
(t = 1.0ms)	I t	25	A S
Critical rate of rise of on-state current	di/dt	10	A/μs
Peak gate power (pulse width = 10μs)	P <sub>GM</sub>	10	Watts
Average gate power (T <sub>C</sub> = 80°C, t = 8.3ms)	P <sub>G(AV)</sub>	0.5	Watts
Peak gate current (pulse width = 10μs)	I <sub>GM</sub>	3.5	Amps
Operating junction temperature range	T <sub>J</sub>	-40 to +100	°C
Storage temperature range	T <sub>stg</sub>	-40 to +125	°C

### THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal resistance, junction to case	R <sub>eJC</sub>	2.2	°C/W

#### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C and either polarity of MT2 to MT1 voltage unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak off state current (1)					
(V <sub>D</sub> = V <sub>DRM</sub> , gate open)					mA
T <sub>C</sub> = 25°C	I <sub>DRM</sub>	-	-	0.1	IIIA
$T_C = 100$ °C		-	-	0.5	
Peak on-state voltage (1)					
(I <sub>TM</sub> = 11A peak, pulse width ≤ 1 ms, duty cycle ≤ 2%)	$V_{TM}$	-	-	1.55	Volts
(I <sub>TM</sub> = 17A peak, pulse width ≤ 1 ms, duty cycle ≤ 2%)		-	-	1.65	
Critical rate of rise of off-state voltage (1)	al / al.				1//
$(V_D = Rated V_{DRM}, gate open, exponential waveform, T_C = 100°C)$	dv/dt	-	200	-	V/µs
Critical rate of rise of commutating voltage (1)					
$(I_{T(RMS)} = Rated RMS on state current, V_{DRM} = Rated peak off state voltage, gate open, commutating di/dt = 6.5A/ms, T_C = 80^{\circ}C)$	dv/dt(c)	4	-	-	V/µs



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DC gate trigger current (continuous dc) (2)					
$(V_D = 12V, \text{trigger mode})$					
$MT2(+)$ , $G(+)$ , $R_1 = 100\Omega$				50	
$MT2(-), G(-), R_L = 100\Omega$		_	_	50	
$MT2(+), G(-), R_1 = 50\Omega$	I <sub>GT</sub>	_	_	50	mA
$MT2(+), G(+), R_1 = 50\Omega, T_C = -40^{\circ}C$		_	_	80	
MT2(-), G(-), R <sub>1</sub> = $50\Omega$ , T <sub>C</sub> = $-40^{\circ}$ C		_	_	80	
		_	-		
MT2(+), G(-), $R_L = 25\Omega$ , $T_C = -40^{\circ}C$		-	-	80	
DC gate trigger voltage (continuous dc) (2)	$V_{GT}$				Volts
(V <sub>D</sub> = 12V, trigger mode)					
$MT2(+), G(+), R_L = 100\Omega$		-	-	2.5	
$MT2(-), G(-), R_L = 100\Omega$		-	-	2.5	
$MT2(+), G(-), R_L = 50\Omega$		-	-	2.5	
MT2(+), G(+), $R_L = 50\Omega$ , $T_C = -40^{\circ}C$		-	-	3.5	
MT2(-), G(-), $R_L = 50\Omega$ , $T_C = -40^{\circ}C$		-	-	3.5	
MT2(+), G(-), $R_L = 25\Omega$ , $T_C = -40^{\circ}C$		-	-	3.5	
MT2(+), G(+), $R_L = 1000\Omega$ , $T_C = 100^{\circ}C^{(3)}$		0.20	-	-	
MT2(-), G(-), $R_L = 1000\Omega$ , $T_C = 100^{\circ}C^{(3)}$		0.20	-	-	
MT2(+), G(-), $R_L = 1000\Omega$ , $T_C = 100^{\circ}C^{(3)}$		0.20	-	-	
MT2(-), G(+), $R_L = 1000\Omega$ , $T_C = 100^{\circ}C^{(3)}$		0.20	-	-	
Holding current (1)	I <sub>H</sub>				mA
(main terminal voltage = 24V, peak initiating current = 0.5A, pulse width = 1ms, duty cycle $\leq$ 2%, gate trigger source = 7V, $20\Omega$ , $T_c = 25$ °C)		-	-	50	
(main terminal voltage = 24V, peak initiating current = 0.5A, pulse width = 1ms, duty cycle $\leq$ 2%, gate trigger source = 7V, $20\Omega$ , $T_c = -40^{\circ}C$ )		-	-	100	
Latching current (2)	Ι <sub>L</sub>				mA
(main terminal voltage = 24V, gate trigger source = 15V, 100Ω, trigger mode)					
MT2(+), G(+)		-	-	100	
MT2(-), G(-)		-	-	100	
MT2(+), G(-)		-	-	200	
MT2(+), G(+), T <sub>C</sub> = -40°C		-	-	200	
MT2(-), G(-), $T_c = -40$ °C		-	-	200	
MT2(+), G(-), $T_c = -40^{\circ}C$		-	-	400	
Note 1: Values apply for either polarity of Main Terminal 2 characteristics referenced to Main Terminal 1.	L	I.	I.	l	L

Note 1: Values apply for either polarity of Main Terminal 2 characteristics referenced to Main Terminal 1. Note 2: Main Terminal 1 is the reference terminal.

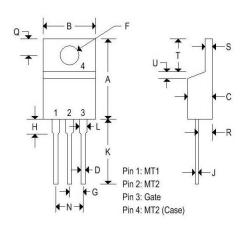
Note 3: With  $V_{\text{\scriptsize D}}$  equal to rated off-state voltage.



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### MECHANICAL CHARACTERISTICS

Case	TO-220AB
Marking	Alpha-numeric
Pin out	See below



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		TO-220AB			
	Inc	hes	Millim	eters	
	Min	Max	Min	Max	
Α	0.575	0.620	14.600	15.750	
В	0.380	0.405	9.650	10.290	
С	0.160	0.190	4.060	4.820	
D	0.025	0.035	0.640	0.890	
F	0.142	0.147	3.610	3.730	
G	0.095	0.105	2.410	2.670	
Н	0.110	0.155	2.790	3.930	
J	0.014	0.022	0.360	0.560	
K	0.500	0.562	12.700	14.270	
L	0.045	0.055	1.140	1.390	
N	0.190	0.210	4.830	5.330	
Q	0.100	0.120	2.540	3.040	
R	0.080	0.110	2.040	2.790	
S	0.045	0.055	1.140	1.390	
T	0.235	0.255	5.970	6.480	
U	-	0.050	97	1.270	
٧	0.045		1.140	-	
Z	- 2	0.080	191	2.030	