

## C228, C22803, C229 SERIES

### SILICON CONTROLLED RECTIFIER

#### **FEATURES**

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak repetitive off state voltage <sup>(1)</sup>			
(T <sub>J</sub> = -40 to +125°C)			
C228F, C228F3, C229F		50	
C228A, C228A3, C229A		100	
C228B, C228B3, C229B	$V_{RRM}$ , $V_{DRM}$	200	Volts
C228C, C228C3, C229C		300	
C228D, C228D3, C229D		400	
C228E, C228E3, C229E		500	
C228M, C228M3, C229M		600	
Peak non-repetitive reverse voltage			
(T <sub>J</sub> = -40 to +125°C)			
C228F, C228F3, C229F		75	
C228A, C228A3, C229A		150	
C228B, C228B3, C229B	$V_{RSM}$	300	Volts
C228C, C228C3, C229C		400	
C228D, C228D3, C229D		500	
C228E, C228E3, C229E		600	
C228M, C228M3, C229M		720	
Forward current RMS	I <sub>T(RMS)</sub>	35	Amps
Peak surge current			Amns
(one cycle, 60Hz, T <sub>C</sub> = -40 to +125°C)	I <sub>TSM</sub>	300	Amps
Circuit fusing considerations	l <sup>2</sup> t		$A^2s$
(T <sub>c</sub> = -40 to +125°C, t = 8.3ms)	11	370	A S
Peak gate power	P <sub>GM</sub>	5	Watts
Average gate power	P <sub>G(AV)</sub>	0.5	Watts
Peak forward gate current	I <sub>GM</sub>	2	Amps
Operating junction temperature range	T <sub>J</sub>	-40 to +125	°C
Storage temperature range	T <sub>stg</sub>	-40 to +150	°C
Mounting torque		30	In. lb.

Note 1:  $V_{DRM}$  and  $V_{RRM}$  for all types can be applied on a continuous basis without incurring damage. Ratings apply for zero or negative gate voltage. Devices shall not have a positive bias applied to the gate concurrently with a negative potential on the anode.

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Maximum	Unit
Thermal resistance, junction to case			
C228 and C229 SERIES	R <sub>eJC</sub>	1.70	°C/W
C228()3 SERIES		1.85	



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### **ELECTRICAL CHARACTERISTICS** (T<sub>c</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Peak forward or reverse blocking current					
(Rated V <sub>DRM</sub> or V <sub>RRM</sub> , gate open)					
$T_C = 25^{\circ}C$	I <sub>DRM</sub> , I <sub>RRM</sub>	-	-	10	μΑ
T <sub>C</sub> = 125°C		-	-	3	mA
Forward "on" voltage	.,				Malka.
(I <sub>TM</sub> = 100A peak)	V <sub>T</sub>	-	-	1.9	Volts
Gate trigger current (continuous dc)					
$(V_D = 12V, R_L = 80\Omega, T_C = 25^{\circ}C)$	I <sub>GT</sub>	-	-	40	mA
$(V_D = 6V, R_L = 80\Omega, T_C = -40^{\circ}C)$		-	-	80	
Gate trigger voltage (continuous dc)					
$(V_D = 12V, R_L = 80\Omega, T_C = 25^{\circ}C)$	V	-	-	2.5	Volts
$(V_D = 6V, R_L = 80\Omega, T_C = -40^{\circ}C)$	$V_{GT}$	-	-	3	
Gate trigger voltage					Valta
(Rated $V_{DRM}$ , $R_L = 1000\Omega$ , $T_C = 125$ °C)	$V_{GT}$	0.2	-	-	Volts
Holding current					
(Anode voltage = 24V, gate open)	1				mA
$T_C = 25^{\circ}C$	I <sub>H</sub>	-	-	75	IIIA
$T_C = -40$ °C		-	-	150	
Turn-on time (t <sub>d</sub> +t <sub>r</sub> )					
$(I_{TM} = 35A, I_{GT} = 40mA)$	t <sub>on</sub>	-	1	-	μs
Turn-off time					
$(I_{TM} = 10A, I_R = 10A)$	t <sub>off</sub>	-	20	-	μs
$(I_{TM} = 10A, I_R = 10A, T_C = 100^{\circ}C)$		-	35	-	
Forward voltage application rate	du/d+				\//c
$(T_{c} = 100^{\circ}C)$	dv/dt	-	50	-	V/µs

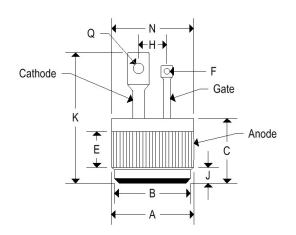


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## SILICON CONTROLLED RECTIFIER

#### MECHANICAL CHARACTERISTICS

Case	Digi PF1 (C229 SERIES)
Marking	Body painted, alpha-numeric



	DIGI PF1			
	Inc	Inches Millimeters		
	Min	Max	Min	Max
Α	0.501	0.505	12.730	12.830
F	-	0.160	-	4.060
G	0.085	0.095	2.160	2.410
Н	0.060	0.070	1.520	1.780
J	0.300	0.350	7.620	8.890
K	-	1.050	-	26.670
L	-	0.670	-	17.020
Q	0.055	0.085	1.400	2.160

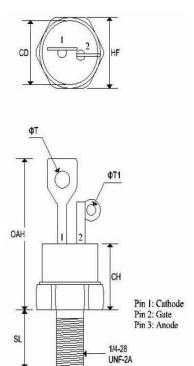


## C228, C22803, C229 SERIES

### SILICON CONTROLLED RECTIFIER

#### MECHANICAL CHARACTERISTICS

Case	TO-48 (C228, C228()3 SERIES)
Marking	Body painted, alpha-numeric
Polarity	Cathode is stud



	TO-48				
	Inc	hes	Millin	Millimeters	
	Min	Max	Min	Max	
CD	-	0.543	-	13.793	
CH	18	0.550	-	13.970	
HF	0.544	0.563	13.817	14.301	
OAH	-	1.193	-	30,303	
SL	0.422	0.453	10.718	11.507	
ΦТ	0.125	0.165	3,175	4.191	
ΦT <sub>1</sub>	0.060	0.075	1.524	1.905	

FIGURE 1 – CURRENT DERATING
(HALF-WAVE RECTIFIED SINE WAVE)

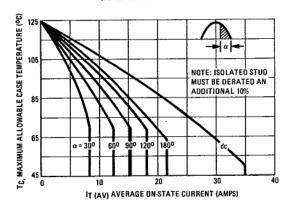


FIGURE 2 - CURRENT DERATING
(FULL-WAVE RECTIFIED SINE WAVE)

