

T6421 SERIES

High-reliability discrete products and engineering services since 1977

BIDIRECTIONAL TRIODE THYRISTORS

FEATURES

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Repetitive peak off-stage voltage, gate open			
(T _J = -65 to +100°C)			
T6421B	V _{DRM}	200	Volts
T6421D	V DRM	400	Volts
T6421M		600	
T6421N		800	
RMS on-state current (conduction angle = 360° , $T_{c} \le 65^{\circ}$ C)	I _{T(RMS)}	30	Amps
Peak non-repetitive surge current (One Cycle, 60Hz)	I _{TSM}	300	Amps
Circuit fusing considerations	l ² t		A ² s
(T ₁ = -65 to +100°C, t = 1.25 to 10ms)		450	AS
Peak gate power (pulse width = 1.0µs)	P _{GM}	40	Watts
Average gate power	P _{G(AV)}	0.75	Watts
Peak gate current (pulse width $\leq 1.0 \mu s$)	I _{GM}	2	Amps
Operating junction temperature range	TJ	-65 to +100	°C
Storage temperature range	T _{stg}	-65 to +150	°C
Stud torque		30	In. lb.

THERMAL CHARACTERISTICS

Characteristics	Symbol	Max	Unit
Thermal resistance, junction to case	R _{eJC}	1.0	°C/W

ELECTRICAL CHARACTERISTICS (T_c = 25°C and either polarity of MT2 to MT1 voltage, unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak off state current (V _D = V _{DRM} , gate open, T _J = 100°C	I _{DRM}	-	-	4	mA
Peak on-state voltage (either direction) (I _{TM} = 100A peak)	V _{TM}	-	2.1	2.5	Volts
DC gate trigger current (continuous dc) $(V_D = 12V, R_L = 30\Omega)$ MT2(+), G(+); MT2(-), G(-) MT2(+), G(-); MT2(-), G(+)	I _{GT}	-	20 35	50 80	mA
DC gate trigger voltage (continuous dc), all trigger modes ($V_D = 12V$, $R_L = 30\Omega$) ($V_D = Rated V_{DRM}$, $R_L = 125\Omega$, $T_c = 100$ °C)	V _{GT}	- 0.2	1.35 -	2.5	Volts
Holding current (V_D = 12V, gate open, I _T = 150mA)	IH	-	-	60	mA
Gate controlled turn on time (V_D = Rated V_{DRM} , I_{TM} = 45A, I_{GT} = 200mA, rise time = 0.1µs)	t _{gt}	-	1.7	3	μs



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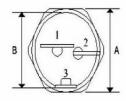
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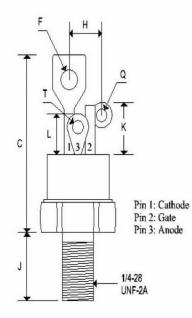
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Critical rate of rise of commutating voltage(commutating di/dt = 16A/ms, gate unenergized, V_D = Rated V_{DRM} , $I_{T(RMS)}$ = 30A, T_c = rated value from figure 1)	dv/dt(c)	3	20	-	V/µs
Critical rate of rise of off-state voltage					
(V_D = Rated V_{DRM} , gate open, exponential waveform, T_C = 100°C)					
T6421B	dv/dt	40			V/µs
T6421D		25	-	-	
T6421M		20			

MECHANICAL CHARACTERISTICS

Case	TO-48 ISO
Marking	Alpha-numeric
Polarity	Cathode is stud





		T0-4	8 ISO	
	Inc	Inches		neters
	Min	Max	Min	Max
A	0.551	0.559	14.000	14.200
В	0.501	0.505	12.730	12.830
C	-	1.280		32.510
F		0.160		4.060
Η	-	0.265	-	6.730
J	0.420	0.455	10.670	11.560
K	0.300	0.350	7.620	8.890
L	0.255	0.275	6.480	6.990
Q	0.055	0.085	1.400	2.160
Τ	0.135	0.150	3.430	3.810



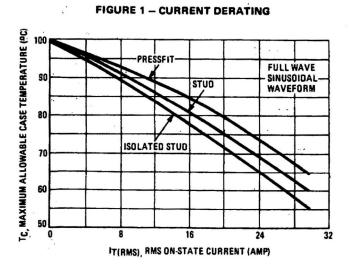
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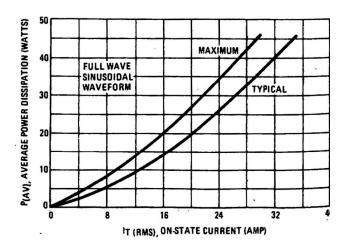
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Rev. 20130205