

# MAC218(A) SERIES

# High-reliability discrete products and engineering services since 1977

# SILICON BIDIRECTIONAL THYRISTORS

### **FEATURES**

- Available as "HR" (high reliability) screened per MIL-PRF-19500, JANTX level. Add "HR" suffix to base part number.
- Available as non-RoHS (Sn/Pb plating), standard, and as RoHS by adding "-PBF" suffix.

## **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak repetitive off-state voltage <sup>(1)</sup>			
(T <sub>J</sub> = 25 to +125°C, gate open)			
MAC218-4, MAC218A-4		200	
MAC218-5, MAC218A-5		300	
MAC218-6, MAC218A-6	$V_{DRM}$	400	Volts
MAC218-7, MAC218A-7		500	
MAC218-8, MAC218A-8		600	
MAC218-9, MAC218A-9		700	
MAC218-10, MAC218A-10		800	
RMS on-state current (conduction angles = 360°, T <sub>C</sub> = 80°C)	I <sub>T(RMS)</sub>	8	Amps
Peak non-repetitive surge current			A
(1 cycle, 60 Hz, $T_c$ = 80°C, preceded and followed by rated current)	I <sub>TSM</sub>	100	Amps
Circuit fusing considerations (t = 8.3ms)	l²t	40	A <sup>2</sup> s
Peak gate power			14/2442
$(T_C = 80^{\circ}C, \text{ pulse width} = 2\mu\text{s})$	P <sub>GM</sub>	16	Watts
Average gate power	D.		Watts
$(T_c = 80^{\circ}C, t = 8.3 ms)$	$P_{G(AV)}$	0.35	Walls
Peak gate trigger current			Amns
(pulse width = 1µs)	I <sub>GTM</sub>	4	Amps
Operating junction temperature range	T <sub>J</sub>	-40 to +125	°C
Storage temperature range	T <sub>stg</sub>	-40 to +150	°C

Note 1: VDBM for all types can be applied on a continuous basis. Blocking voltage shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

#### THERMAL CHARACTERISTICS

THE MINISTERS			
Characteristic	Symbol	Maximum	Unit
Thermal resistance, junction to case	R <sub>eJC</sub>	2.2	°C/W

# **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур.	Max	Unit
Peak blocking current (either direction)					
$(V_D = Rated V_{DRM}, gate open @ T_J = 25^{\circ}C)$	I <sub>DRM</sub>	-	-	10	μΑ
$(V_D = Rated V_{DRM,} gate open @ T_J = 125°C)$		-	-	2	mA
Peak on-state voltage (either direction)  (I <sub>TM</sub> = 11.3A peak, pulse width = 1 to 2 ms, duty cycle ≤ 2%)	V <sub>TM</sub>	-	1.7	2.0	Volts
Gate trigger current (continuous dc)					
$(V_D = 12V, R_L = 12\Omega)$					
Trigger Mode	I <sub>GT</sub>				mA
MT2(+),G(+); MT2(+),G(-); MT2(-),G(-)		-	-	50	
MT2(-),G(+) "A" suffix only		-	-	75	
Gate trigger voltage (continuous dc)	$V_{GT}$				Volts



# High-reliability discrete products and engineering services since 1977

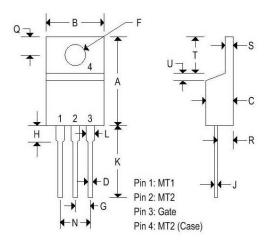
# MAC218(A) SERIES

# SILICON BIDIRECTIONAL THYRISTORS

(main terminal voltage = 12V, $R_L = 100\Omega$ )					
MT2(+),G(+)		-	0.9	2	
MT2(+),G(-)		-	0.9	2	
MT2(-),G(-)		-	1.1	2	
MT2(-),G(+) "A" suffix only		-	1.4	2.5	
(main terminal voltage= Rated $V_{DRM}$ , $R_L = 10k\Omega$ , $T_J = 125^{\circ}C$ )					
MT2(+), G(+); MT2(-), G(-); MT2(+), G(-)		0.2	-	-	
MT2(-), G(+) "A" suffix only		0.2	-	-	
Holding current (either direction)	I <sub>H</sub>				mA
(V <sub>D</sub> = 24V, gate open, initiating current = 200mA)		-	-	50	
Critical rate of rise of commutating off-state voltage	dv/dt(c)	-	5	-	V/µs
$(V_D = Rated V_{DRM}, I_{TM} = 11.3A, commutating di/dt = 4.1A/ms, gate unenergized,$					
$T_C = 80$ °C)					
Critical rate of rise of off-state voltage	dv/dt				V/µs
(V <sub>D</sub> = Rated V <sub>DRM</sub> , exponential voltage rise, gate open, T <sub>J</sub> = 125°C)		-	100	-	

## **MECHANICAL CHARACTERISTICS**

Case TO-220AB			
Marking	Alpha-numeric		
Pin out	See below		



	TO-220AB			
	Inches		Millimeters	
	Min	Max	Min	Max
Α	0.575	0.620	14.600	15.750
В	0.380	0.405	9.650	10.290
C	0.160	0.190	4.060	4.820
D	0.025	0.035	0.640	0.890
F	0.142	0.147	3.610	3.730
G	0.095	0.105	2.410	2.670
Н	0.110	0.155	2.790	3.930
J	0.014	0.022	0.360	0.560
K	0.500	0.562	12.700	14.270
L	0.045	0.055	1.140	1.390
N	0.190	0.210	4.830	5.330
Q	0.100	0.120	2.540	3.040
R	0.080	0.110	2.040	2.790
S	0.045	0.055	1.140	1.390
T	0.235	0.255	5.970	6.480
U	-	0.050	197	1.270
٧	0.045	-	1.140	(4)
Z		0.080	(9)	2.030

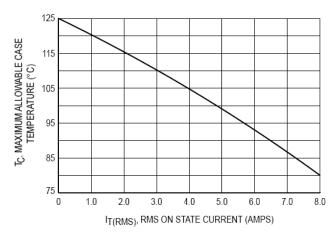


High-reliability discrete products and engineering services since 1977

# MAC218(A) SERIES

## SILICON BIDIRECTIONAL THYRISTORS

### FIGURE 1 — CURRENT DERATING



## FIGURE 2 — POWER DISSIPATION

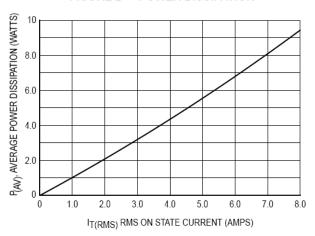
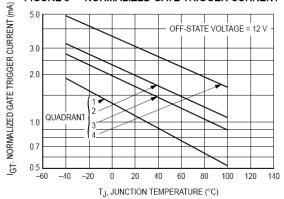
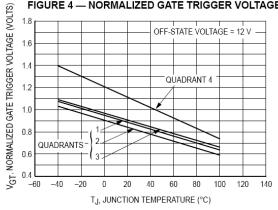


FIGURE 3 — NORMALIZED GATE TRIGGER CURRENT



### FIGURE 4 — NORMALIZED GATE TRIGGER VOLTAGE



# FIGURE 5 — NORMALIZED HOLDING CURRENT

